UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,362,621 B2 Page 1 of 2

APPLICATION NO. : 10/676276 DATED : April 22, 2008

INVENTOR(S) : Bhaskar P. Chatterjee et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5

Line 9, [Claim 1.] "..., comprising; ..." should read --..., comprising: ...--.

Line 14, [Claim 1.] "... driving signal; and" should read --...driving signal trace; and--.

Lines 20-24, [Claim 1.] "... wherein ... is designed to:

determine whether one of said plurality of select signal traces, corresponding to the evaluated bit trace, is asserted; and conditionally decouple ..."

should read --... wherein ... is designed to:

determine whether one of said plurality of local bit traces evaluates; determine if one of said plurality of select signal traces, corresponding to the evaluated bit trace, is asserted; and conditionally decouple ...--.

Lines 63-65, [Claim 8.] "... couple to ..." should read --...coupled to...--.

Column 6

Lines 36-37, "17. A system comprising:

a processor having:

- a register file including:
- a register file output signal trace;
- a driving signal trace; and
- a global bit trace ..."

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a global bit trace ...--.

should read --17. A system comprising:
a processor having:
a register file output signal trace;
a driving signal trace; and
a register file including:

Signed and Sealed this

Twenty-fifth Day of August, 2009

David J. Kappos

David J. Kappos

Director of the United States Patent and Trademark Office